



08/479211

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Docket No. 0756-1363

Date: June 7, 1995

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is a Rule 53(d) continuation-in-part patent application of:

Inventor(s):

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For: METHOD FOR MANUFACTURING
SEMICONDUCTOR DEVICE

Enclosed are:

- ☒ 34 pages of specification, claims and abstract
☒ 34 sheets of drawings (informal) (Figs. 1A-4F)

☐ An assignment of the invention to _____

☐ Declaration and Power of Attorney.

☒ Priority is claimed based on foreign application(s)

No(s): 6-51237Filed on: February 23, 1994Filed in: JapanCertified copy(s) ☐ attached; ☒ filed onFebruary 21, 1995 in prior U.S.S.N. 08/391,580filed February 21, 1995

☐ A verified statement to establish small entity status under 37 CFR
1.9 and 37 CFR 1.27.

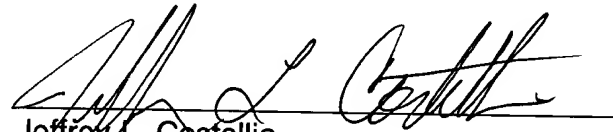
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☒ Other: Preliminary Amendment, Sheet of Filing Particulars

☐ A check in the amount of \$ _____ to cover the filing fee is enclosed.

☒ No filing fee is enclosed.

Respectfully submitted,



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Title: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

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